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Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS

Michael D. Haines

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FILED

December 12, 2001

FOR

METHOD AND APPARATUS FOR INCREASING THE

IMMUNITY OF NEW GENERATION

MICROPROCESSOR FROM ESD EVENTS

GROUP ART UNIT

2826

EXAMINER

Alexander O. Williams

HON. COMMISSIONER FOR PATENTS P.O. Box 1450

Alexandria, VA 22313-1450

TECHNOLOGY CENTER 2800

RESPONSE TO RESTRICTION REQUIREMENT

SIR:

The following remarks are respectfully submitted in response to the Office Action dated August 21, 2003.